UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/779,544	02/13/2004	Sehat Sutardja	MP0450	1588
26703 7590 12/27/2007 HARNESS, DICKEY & PIERCE P.L.C.		EXAMINER		
5445 CORPORATE DRIVE			BUTLER, DENNIS	
SUITE 200 TROY, MI 48098			ART UNIT	PAPER NUMBER
			2115	
		·		
			MAIL DATE	DELIVERY MODE
			12/27/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)				
	10/779,544	SUTARDJA, SEHAT				
Office Action Summary	Examiner	Art Unit				
	Dennis M. Butler	2115				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address						
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status		•				
1) Responsive to communication(s) filed on 01 A	<u>ugust 2007</u> .					
2a) ☐ This action is FINAL . 2b) ☐ This	This action is FINAL . 2b) ☐ This action is non-final.					
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4) Claim(s) 1-14,16-38,40-62 and 64-102 is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-14,16-38,40-62 and 64-102</u> is/are r 7)□ Claim(s) is/are objected to.	rejected.					
8) Claim(s) is/are objected to: 8) Claim(s) are subject to restriction and/o	or election requirement					
Oldimits) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) acc						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) All b) Some * c) None of:						
 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)						
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date Notice of Informal Patent Application						
Paper No(s)/Mail Date 11/27/07.	6) Other:					

10/779,544 Art Unit: 2115

- 1. This action is in response to the amendment received on August 1, 2007 and the IDS received on November 27, 2007. Claims 1-14, 16-38, 40-62 and 64-102 are pending. Claims 15, 39 and 63 have been canceled.
- 2. The text of those sections of Title 35, US Code not included in this action can be found in a prior Office Action.
- 3. Claims 1-4, 6-7, 9-10, 12-14, 16-17, 20, 22-23, 25-28, 31, 33-34, 36-38, 40-41, 44, 46-47, 49-52, 55, 57-58, 60-62, 64-65, 67-68, 70-71, 73-102 are rejected under 35 U.S.C. 102(e) as being clearly anticipated by Bear et al., U. S. Patent Application Publication 2004/0225901.

Bear et al describe the claimed invention including primary (computer 20/processing unit 21) and auxiliary/secondary (auxiliary computing device/auxiliary processor) processing systems with figures 1, 2, 8 and 10. The auxiliary system independently handles activities while the computer is in the standby (inactive) state. The auxiliary processing system can process activities such as answering and recording phone calls, handling voice mail, displaying e-mail, browsing the internet and recording TV shows without having to turn on the computer.

4. Claims 5, 8, 11, 18-19, 21, 24, 29-30, 32, 35, 42-43, 45, 48, 53-54, 56, 59, 66,
69, and 72 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bear et al.,
U. S. Patent Application Publication 2004/0225901.

Claims 5, 8, 11, 18-19, 21, 24, 29-30, 32, 35, 42-43, 45, 48, 53-54, 56, 59, 66, 69, and 72 recite obvious variations of well known data processing elements and procedures and these claims would have been obvious to one of ordinary skill in the art in view of

10/779,544

Art Unit: 2115

Bear's teachings and suggestions of auxiliary processors, displays, input devices and memory devices. The location/placement of the auxiliary processing system is a matter of design choice as Bear describes and claims that the auxiliary processor can be coupled to a computing device or housed within the computing device. Bear describes providing an input device for the auxiliary computing system. A touch pad is a well known input device and it would have been obvious to use a touch pad as the auxiliary input device.

Response to Arguments

5. Applicant's arguments filed on August 1, 2007 have been fully considered but they are not persuasive.

In the Remarks, applicant has argued in substance that:

- A. Bear fails to show, teach or suggest a secondary processor that dissipates less power than the primary processor. Bear is silent as to the relative power dissipation of the alleged primary and secondary processors.
- 6. As to point A, the examiner disagrees with applicant's assertions. Bear clearly shows, teaches and suggests that the secondary processor dissipates less power than the primary processor as claimed. Bear discloses that processing unit 21 (primary processor) processes information and executes programs when the computer is in the on state and also when performing background tasks with computer 20 of figure 2 and at paragraphs 16, 35 and 36. Processing unit 21 is clearly disclosed as the primary processing unit that performs primary processing in both the foreground and

10/779,544 Art Unit: 2115

background modes. Bear discloses that auxiliary processor 12 is used for auxiliary processing of information at paragraphs 60 and 72. Bear discloses that auxiliary processor is used in the standby state at paragraph 42. The standby state is entered when computer 20 is inactive and it is a low power state (paragraphs 44 and 45). Therefore, auxiliary processor is used when computer 20 is powered down into the standby mode (claims 1 and 33) and wakes processing unit 21 (primary processor) when there is a request for service for performing primary processing (claims 19-24). Auxiliary processor is a low power processor designed to operate in the low power standby state and it wakes the primary processor when primary (high power) foreground or background processing is required. In addition, Bear discloses turning the fan off when in the standby state when only the auxiliary processor is operating (paragraph 45, claim 5) and turning the fan back on when entering the on state and performing primary processing (paragraph 73). Therefore, the auxiliary processor clearly uses less power that the primary processor (processing unit 21). Bear further discloses that auxiliary processor may be an ARM processor that may be integrated with the central processor unit as a system on a chip at paragraph 39. Bear clearly discloses that processing unit 21 is the central processing unit of computer that operates in the high power on state with the fan on while auxiliary processor operates in the low power standby state with the fan off, has limited display and processing functionality (figures 8-10 displaying summary information) and has limited power dissipation (no fan to prevent overheating, required for high power processing). Bear clearly shows, teaches and suggests that the secondary processor dissipates less power than the primary processor as claimed.

Application/Control Number:

10/779,544 Art Unit: 2115

Conclusion

7. THIS ACTION IS MADE FINAL. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dennis M. Butler whose telephone number is 571-272-3663. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

Application/Control Number:

10/779,544 Art Unit: 2115 Page 6

you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Dennia M. Butler
Dennis M. Butler
Primary Examiner

Art Unit 2115